

IN THE SPECIFICATION:

Please replace the paragraph at page 2, lines 19-24, with the following rewritten paragraph:

Q1
In view of the above, it is an object of the present invention to provide a serial bus data control device capable of obtaining each piece of actual data contained in each of the packets needed for reconstructing a series of original data by storing actual data contained in two or more received packets in a memory location in a manner such that each piece of the actual data has a continued address in a buffer.

Please replace the paragraph at page 9, lines 21-26, with the following rewritten paragraph:

Q2
FIG. 1 is a schematic block diagram showing ~~configurations~~ the configurations of a serial bus data control device 10 according to a first embodiment of the present invention. As shown in FIG. 1, the serial bus data control device 10, to transfer data through the IEEE 1394 bus 300, for example, between a computer 100 and a node 200 ~~being in~~ peripheral equipment, is connected to a local bus (not shown) within the computer 100.

Please replace the paragraph starting at page 9, lines 27 through page 10, line 12, with the following rewritten paragraph:

Q3
Among a plurality of nodes connected via the IEEE 1394 bus 300 ~~are received and sent~~ send or receive two or more packets each containing actual data obtained by dividing a series of data into data having a predetermined data length. A packet is composed of a header having a predetermined length containing information about conditions of the sending and receiving

Q3
nodes, actual data positioned subsequently to the header and a footer also having a predetermined length and being positioned subsequently to the actual data constituting a tail portion of the packet. Each of data size of a header, actual data and footer is a predetermined size, that is, each of the header, actual data and footer is constructed so that the data size of each of the header, actual data and footer is, for example, an integral multiple of a storing unit of the buffer memory included in each node, i.e., of a capacity of each addressed memory location in the buffer.

Please replace the paragraph starting at page 10, line 13 through page 11, line 12, with the following rewritten paragraph:


Q4
As shown in FIG. 1, the serial bus data control device 10 of the first embodiment connected to the node 200 ~~being the peripheral device~~ is composed of a physical layer control section 11, a link layer control section 12 and a buffer 13 to temporarily store data. The physical layer control section 11, as in the case of the conventional serial bus data control device, is adapted to control receiving and sending of data in accordance with standards designated for the IEEE 1394 bus regarding signal voltages, electrical characteristics of a modem and a network interface card or a like, and physical characteristics of a connector format and numbers of pins or likes. The link layer control section 12 provides procedures for controlling data transferred among nodes. The link layer control section 12, as is well known, is a section adapted to perform preprocessing of storing a plurality of packets fed through the physical layer control section 11 from the IEEE 1394 bus 300 sequentially in the buffer 13 and to divide the received packet including a header, actual data and footer into unit length data each having a predetermined

Q4
length. The unit length data size matches a storing unit in the buffer 13, i.e., a capacity of an addressed memory location in the buffer 13. The link layer control section 12, also as is well known, is adapted to address of a memory location to store unit length data in the buffer 13. The buffer 13 stores each piece of unit length data in the memory location addressed by the link layer control section 12. The buffer 13, as in the case of the conventional serial bus data control device, temporarily stores unit length data before the unit length data is sent, as parallel data, to the local bus in the computer 100. The link layer control section 12 has an address control circuit 1 as shown in FIG. 1. The address control circuit 1 is a buffer address control means to assign an address of a memory location in the buffer 13 to each unit length data of a header, actual data and a footer contained in each of the two or more received packets.

Please replace the paragraph at page 12, lines 17-27, with the following rewritten paragraph:

Q5
Therefore, the increment signal generating section 2 is put in an operation state while addressing to store all data including a header to a footer contained in each of the packets is performed in response to the operation signal fed by the enable register 5, while the decrement signal generating section 3 not receiving the operation signal from the enable register 5 is put in a no operation state. After addressing to store a footer contained in each of the ~~packet~~ packets has been terminated and before addressing to store a subsequent packet is performed, the decrement signal generating section 3 is put in an operation state in response to the operation signal fed from the enable register 5, while the increment signal generating section 2 not receiving the operation signal is put in a no operation state.

Please replace the paragraph starting at page 19, line 21 through page 20, line 1, with the following rewritten paragraph:

 A serial bus data control device of a second embodiment, as in the case of the first embodiment, to transfer data through the IEEE 1394 bus, between a computer and other nodes, is connected to local buses within the computer. The serial bus data control device of the second embodiment has a physical layer control section 11, a link layer control section 12 and a buffer 13 as in the case of the first embodiment shown in FIG. 1. In the second embodiment, the same reference numbers are assigned to parts having the same function as in the first embodiment.

Please replace the Abstract, on page 47, with the rewritten Abstract which is presented on a separate page attached to this Amendment (a clean copy of the rewritten Abstract is also attached).